

<b>Notice of References Cited</b>	Application/Control No. 10/731,341		Applicant(s)/Patent Under Reexamination FLOYD, BRIAN ALLAN	
	Examiner Khanh V. Nguyen		Art Unit 2817	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,166,599	12-2000	Aparin et al.	330/302
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tang et al., "A 1.2 V, 1.8 GHz CMOS two-stage LNA with common-gate amplifier as an input stage", ASIC, 2003, Proceedings, 5 <sup>th</sup> International Conference on Volume 2, 21-24 Oct. 2003, Page 1044, IEEE
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.